



2822

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : S. Kikuchi et al.
Serial No. : 09/837,397
Filed : April 18, 2001
Title : SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME

Art Unit : 2822
Examiner : Monica Lewis

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO ACTION OF AUGUST 11, 2003

REMARKS

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Claims 1-12 are pending in this application. Claims 7-12 have been withdrawn. Claims 1-6 have been rejected.

Claim Rejections – 35 USC 103

Claims 1 to 6 have been rejected as being obvious over Hsing et al. in view of Beasom and in view of Kuroi et al.

We respectfully disagree for at least the following reasons.

Claim 1 recites a semiconductor device comprising:

- a semiconductor substrate of a first conductive type;
- a gate insulation film disposed over the semiconductor substrate;
- a gate electrode provided on the gate insulation film;
- a high concentration source region of a second conductive type disposed in the semiconductor substrate and at one end of said gate electrode;

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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